



vs

Open vSwitch

December 10th-11th, 2020 | Virtual, Earth

**More Next Steps for Even Higher  
Performance on the Software Datapath**

Harry van Haaren  
Intel

# Topics In Todays Video

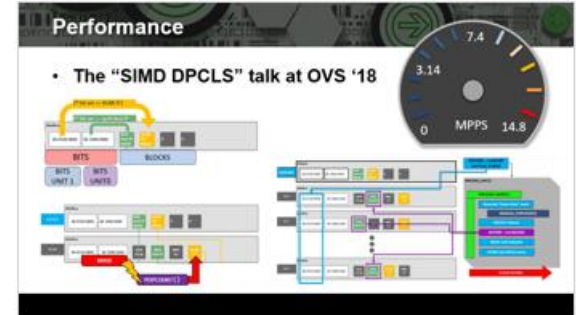
- **DPIF**
  - Optimizing the glue code of the Userspace Datapath
- **MFEX**
  - Optimizing the packet parsing using AVX512 SIMD instructions

**Please note the video explains details not in these slides!**

# Context - Previous Talks on SW Datapath

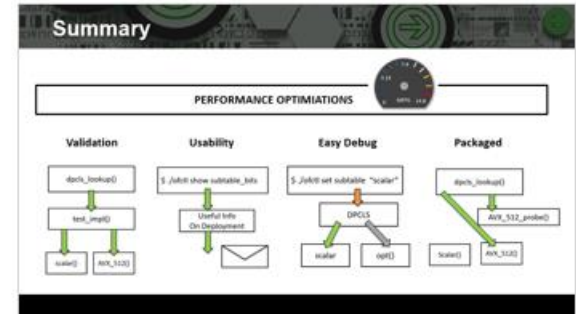
- **OVS Conf '18**

- Miniflow Datastructure Optimization
- DPCLS Lookup Optimizations in AVX512
- <https://youtu.be/5-MDlpUIOBE>



- **OVS Conf '19**

- Testing Validation, and Deployment
- Packaging with ISA-optimizations
- <https://youtu.be/x0bOpojnpmU>

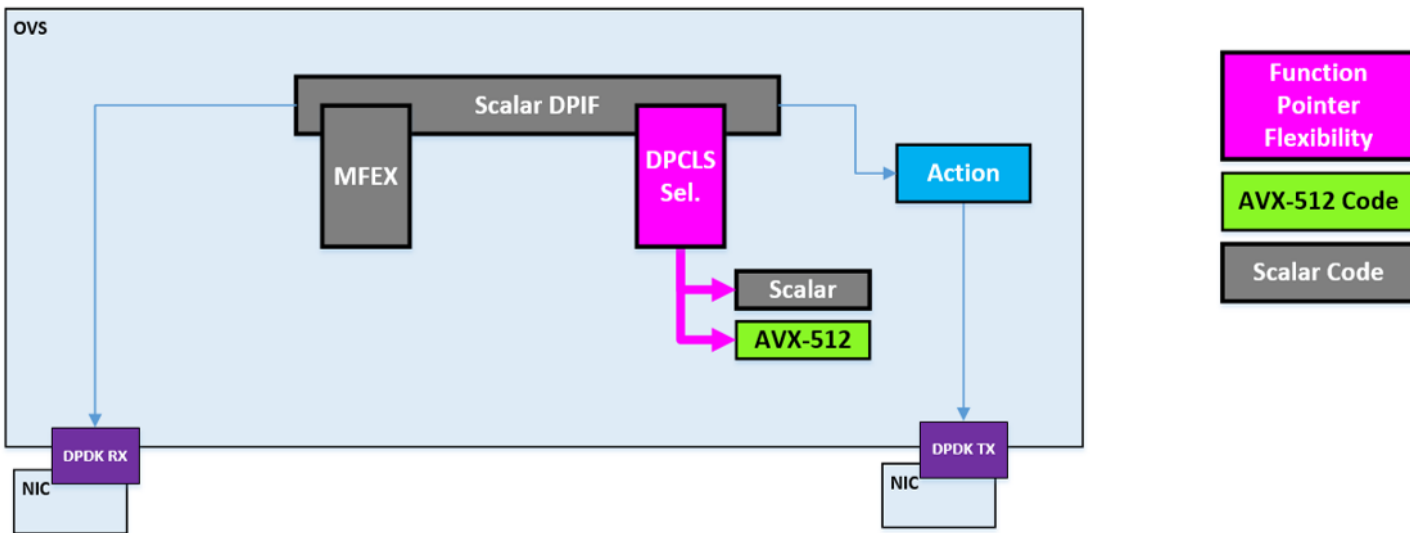




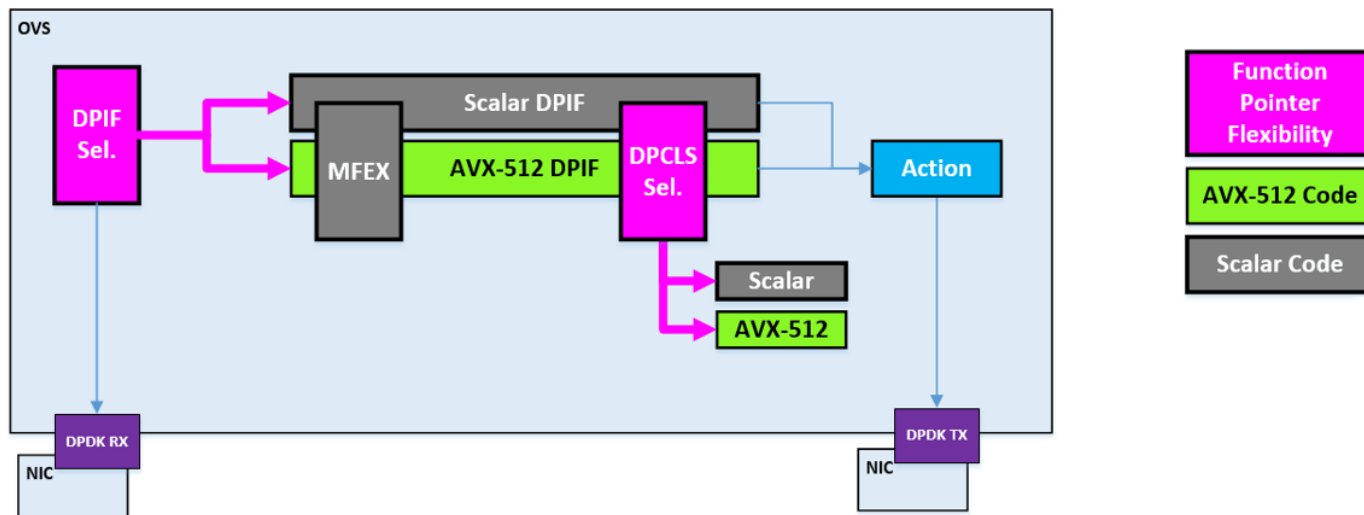
- **DPIF**

- Optimizing the glue code of the Userspace Datapath

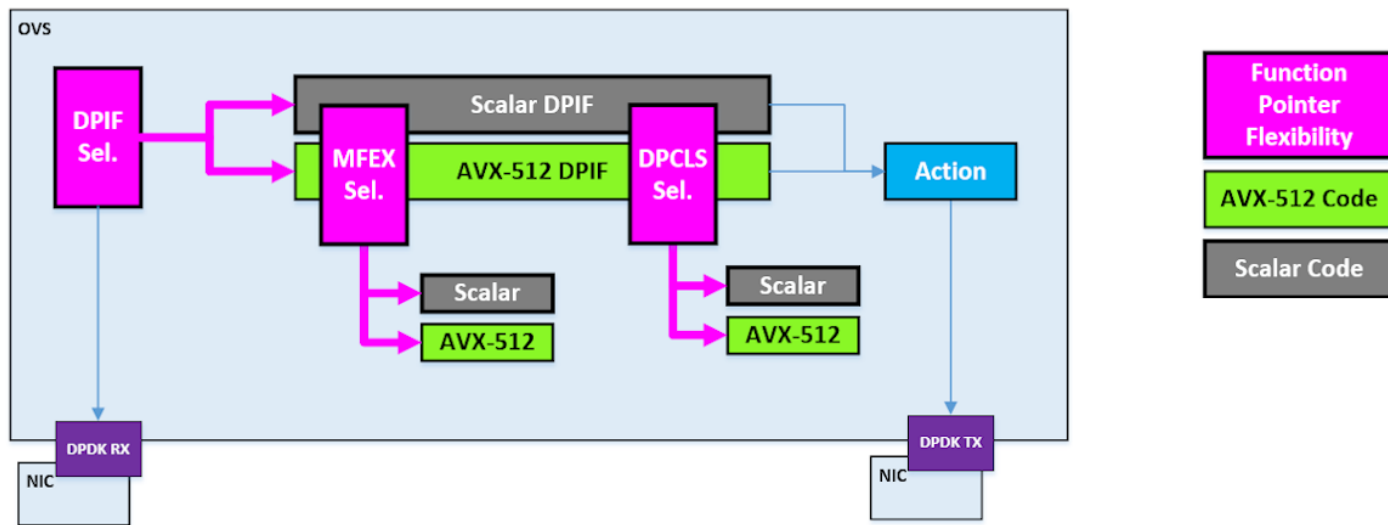
# OVS – SW Datapath Today



# OVS 2.14 + DPIF



## 2.14 + DPIF + MFEX



## DPIF AVX-512

```
ovs-appctl dpif-netdev/dpif-set dpif_avx512
```

```
ovs-appctl dpif-netdev/dpif-set dpif_scalar
```

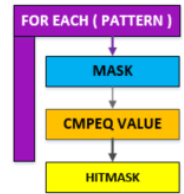
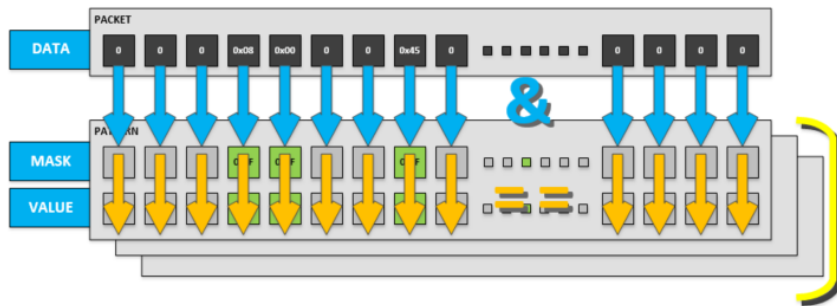


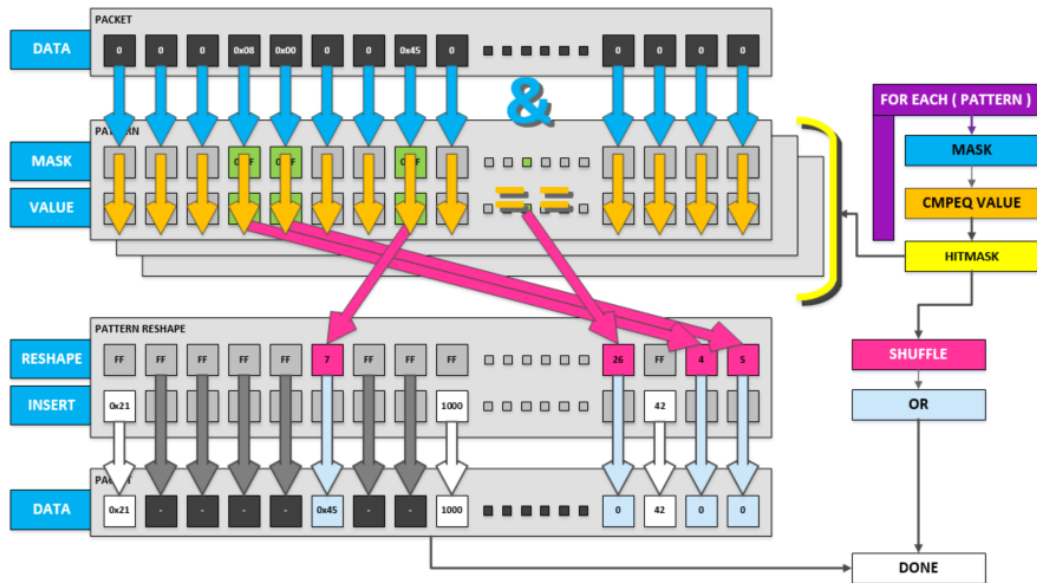


- **MFEX**

- Optimizing the glue code of the Userspace Datapath







## Miniflow Extract with AVX512

```
ovs-appctl dpif-netdev/miniflow-optimize avx512
  match_mask=0xFFFF
  match_data=0xf00d
  shuffle_index=0x1,0x4,0x2,0x3
  test_packet=<Ether()/IP()/UDP(>>
```



**! Thanks !**  
**? Questions ?**

**Harry van Haaren**  
**[harry.van.haaren@intel.com](mailto:harry.van.haaren@intel.com)**